

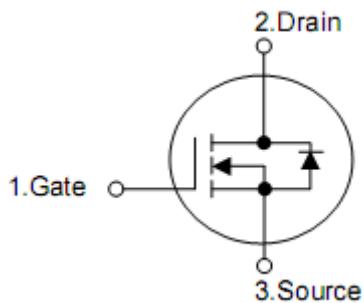
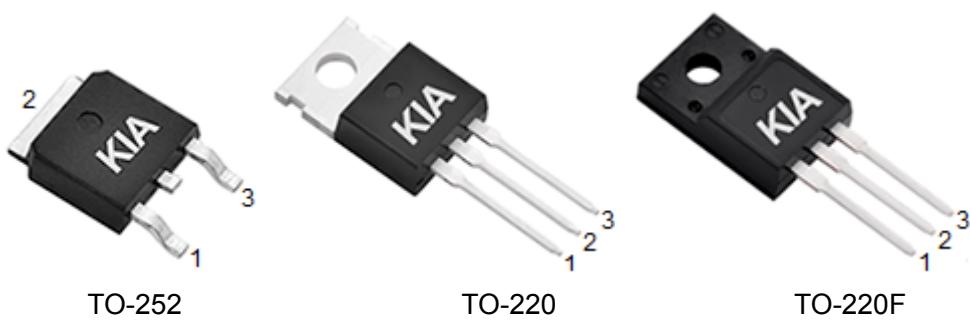
1. Product Features

- RoHS Compliant
- $R_{DS(ON)}=7.0\Omega$ (typ.) @ $V_{GS}=10V$
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

2. Applications

- Adaptor
- Charger
- SMPS Standby Power

3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source

4. Ordering Information

Part Number	Package	Brand
KND42120A	TO-252	KIA
KNP42120A	TO-220	KIA
KNF42120A	TO-220F	KIA

5. Absolute maximum ratings

(T_c= 25 °C , unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage T _J =25 °C	V _{DSS}	1200	V
Gate-to-Source Voltage	V _{GSS}	±30	V
Continuous Drain Current @ T _c =25 °C	I _D	3.0	A
Pulsed Drain Current at V _{GS} =10V Limited by T _{Jmax}	I _{DM}	12	A
Single Pulse Avalanche Energy(V _{DD} =50V)	EAS	100	mJ
Maximum Power Dissipation	P _D	75	W
Max. Junction Temperature	T _{Jmax}	150	°C
Storage Temperature Range	T _{STG}	-55 to 150	°C

6. Thermal characteristics

Parameter	Symbol	Ratings	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	1.67	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	75	°C/W

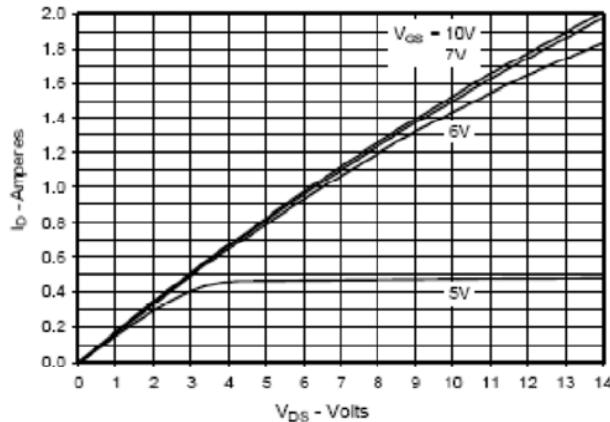
7. Electrical characteristics

($T_J=25^\circ\text{C}$, unless otherwise specified)

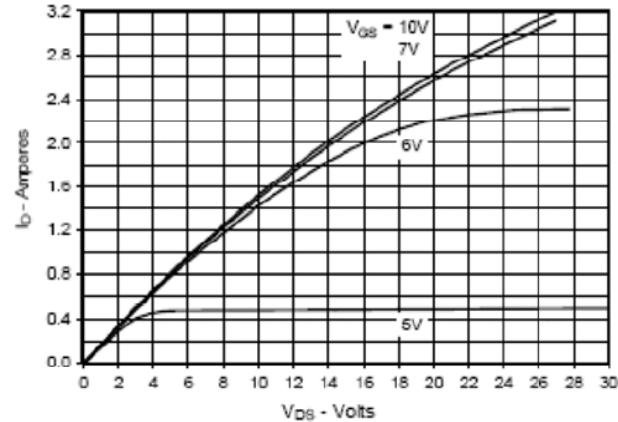
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-to-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	1200	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=1200\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-to-Source Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-100	-	100	nA
Drain-to-Source ON Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.5\text{A}$	-	7.0	9.0	Ω
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.5	-	4.5	V
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, f=1.0\text{MHZ}$	-	860	-	pF
Reverse Transfer Capacitance	C_{rss}		-	22	-	
Output Capacitance	C_{oss}		-	60	-	
Total Gate Charge	Q_g	$\text{V}_{\text{DD}}=600\text{V}, \text{I}_D=3.0\text{A}, \text{V}_{\text{GS}}=10\text{V}$	-	17.5	-	nC
Gate-to-Source Charge	Q_{gs}		-	5.0	-	
Gate-to-Drain (Miller) Charge	Q_{gd}		-	5.5	-	
Turn-on Delay Time	$t_{\text{d(ON)}}$	$\text{V}_{\text{DD}}=600\text{V}, \text{I}_D=3.0\text{A}, \text{R}_G=4.7\Omega, \text{V}_{\text{GS}}=10\text{V}$ (Resistive Load)	-	17	-	nS
Rise Time	t_{rise}		-	6.0	-	
Turn-Off Delay Time	$t_{\text{d(OFF)}}$		-	23	-	
Fall Time	t_{fall}		-	11	-	
Continuous Source Current	I_{SD}	--	-	-	3.0	A
Forward Voltage	V_{SD}	$\text{I}_S=3.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1.5	V
Reverse recovery time	t_{rr}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_F=3.0\text{A}, \text{dI}/\text{dt}=-100\text{A}/\mu\text{s}$	-	200	-	ns
Reverse recovery charge	Q_{rr}		-	760	-	nC

8. Test circuits and waveforms

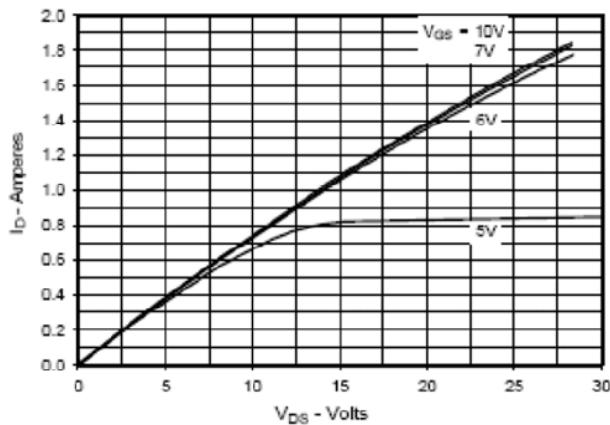
**Fig. 1. Output Characteristics
@ 25°C**



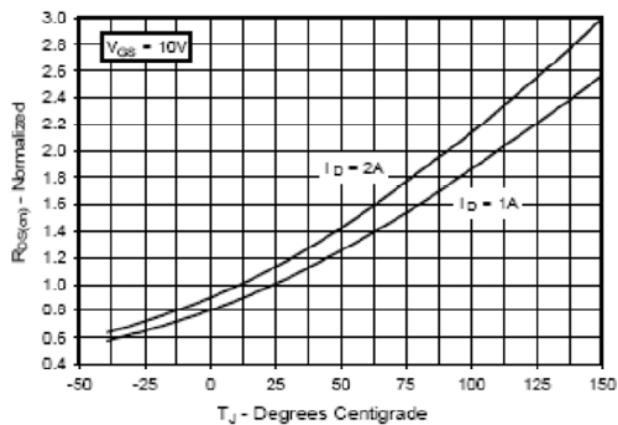
**Fig. 2. Extended Output Characteristics
@ 25°C**



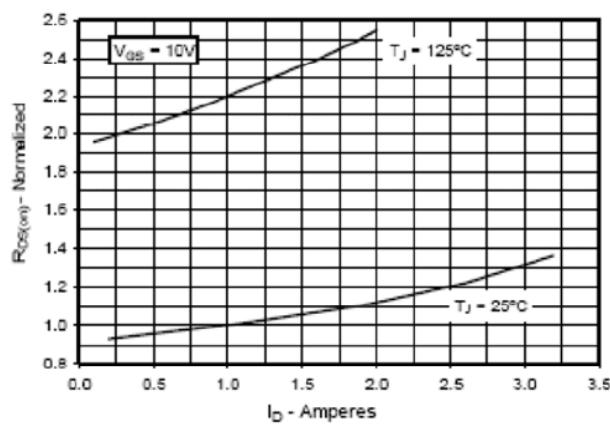
**Fig. 3. Output Characteristics
@ 125°C**



**Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 1A$ Value
vs. Junction Temperature**



**Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 1A$ Value
vs. Drain Current**



**Fig. 6. Maximum Drain Current vs.
Case Temperature**

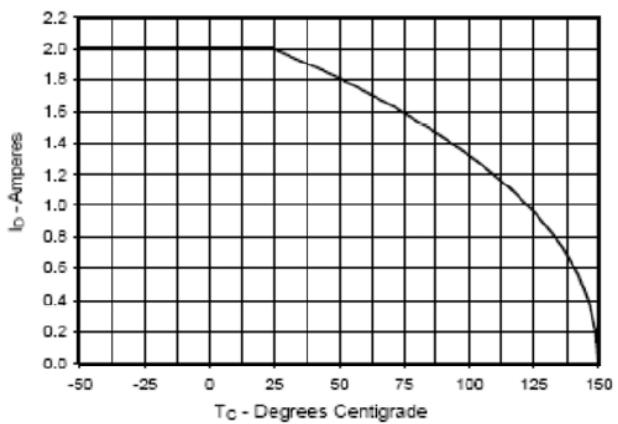


Fig. 7. Input Admittance

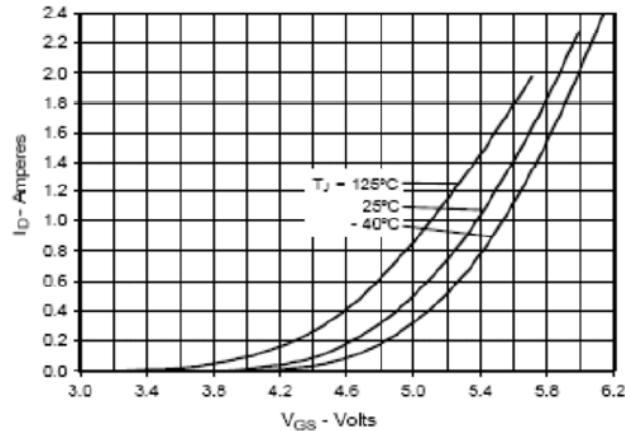


Fig. 8. Transconductance

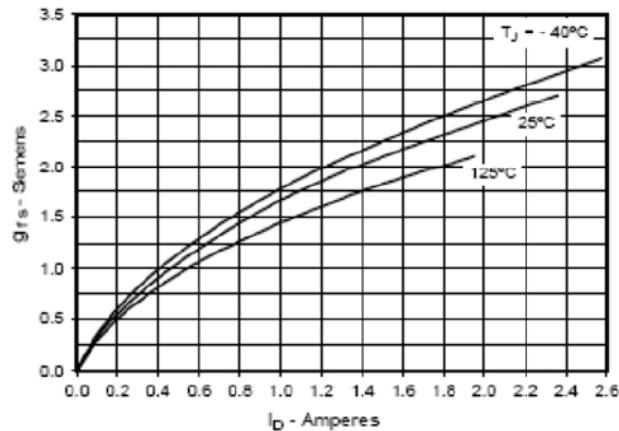


Fig. 9. Forward Voltage Drop of Intrinsic Diode

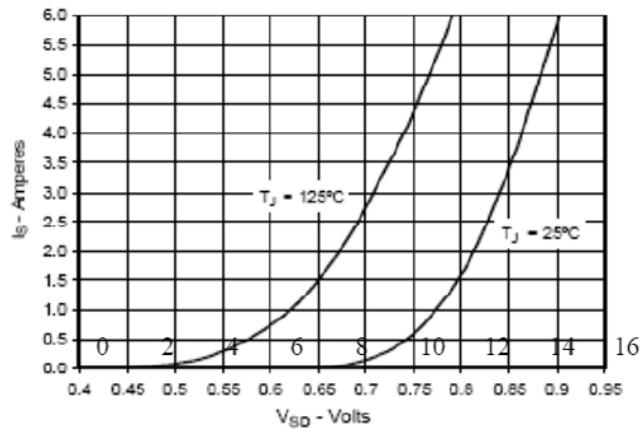


Fig. 10. Gate Charge

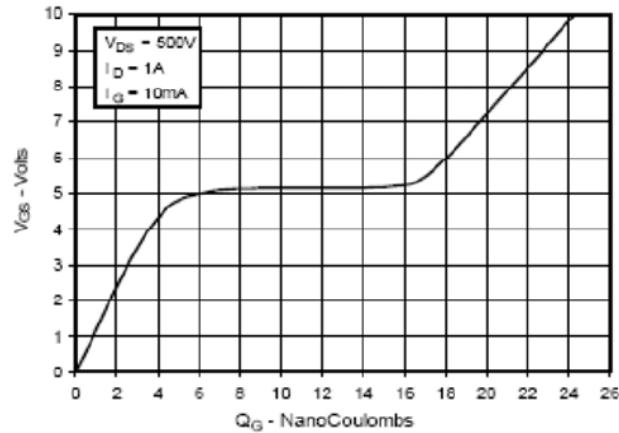


Fig. 11. Capacitance

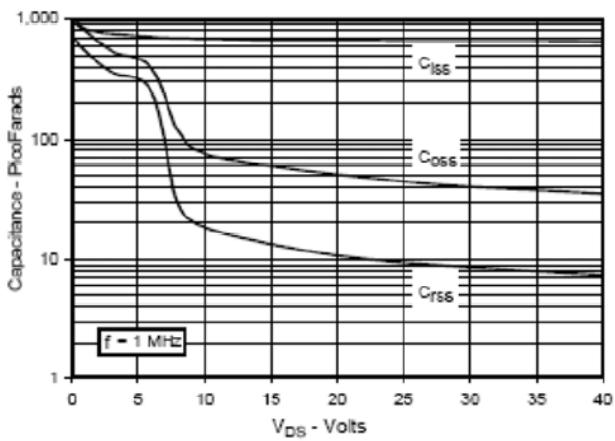


Fig. 12. Maximum Transient Thermal Impedance

